



Complete if Known

Application / Conf. No.	10/769,591 / 8956
Filing Date	January 30, 2004
First Named Inventor	Chidamber R. Kulkarni
Art Unit	2825
Examiner Name	Binh C. Tat
Attorney Docket Number	X-1557-3 US

(use as many sheets as necessary)

Sheet	1	of	3
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U.S. PATENT DOCUMENTS					
Examiner Initials *	Cite No.¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code (if known)			
		US- 5,867,180 A	02-02-99	Katayama et al.	
		US- 6,078,736	06-20-00	Guccione	
		US- 6,182,183 B1	01-30-01	Wingard et al.	
		US- 6,230,307 B1	05-08-01	Davis et al.	
		US- 6,324,629 B1	11-27-01	Kulkarni et al.	
		US- 6,405,160 B1	06-11-02	Djaja et al.	
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		US- 2003/0033374 A1	02-13-03	Horn et al.	
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		US- 2004/0006584 A1	01-08-04	Vandeweerd	
		US- 2004/0128120 A1	07-01-04	Coburn et al.	
		US- 2005/0034090 A1	02-10-05	Sato et al.	
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		US- 2005/0172085 A1	08-04-05	Klingman	

[illegible]

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application / Conf. No.	10/769,591 / 8956
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OTHER -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Gordon Brebner; "Multithreading for Logic-Centric Systems"; 12th International Conference on Field Programmable Logic and Applications; Montpellier, France; 2-4 September 2002; Springer LNCS 2438; pp. 5-14.	
		Gordon Brebner; "Single-chip Gigabit Mixed-version IP Router on Virtex-II Pro"; Proceedings of the 10th Annual IEEE Symposium on Field-Programmable Custom Computing Machines (FCCM'02); April 2002; IEEE Computer Science Press; pp. 35-44.	
		Xilinx, Inc.; U.S. Patent Application Serial No. 10/402,659 filed on March 28, 2003 by James-Roxby.	
		Xilinx, Inc.; U.S. Patent Application Serial No. 10/420,652 filed on April 21, 2003 by Brebner.	
		Xilinx, Inc.; U.S. Patent Application Serial No. 10/421,013 filed on April 21, 2003 by Brebner.	

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